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<b>Notice of Allowability</b>	Application No.	Applicant(s)	
	10/676,822	WHITTED, WILLIAM H.	
	Examiner	Art Unit	
	Dameon E. Levi	2841	

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 08/19/2005 Amendment, 11/18/2005 Interview.
2. ☒ The allowed claim(s) is/are 1-22.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some\* c) ☐ None of the:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

- |   |  |
|---|--|
| 1. <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)   | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)                      |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                | 6. <input checked="" type="checkbox"/> Interview Summary (PTO-413),<br>Paper No./Mail Date _____ |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),<br>Paper No./Mail Date _____ | 7. <input checked="" type="checkbox"/> Examiner's Amendment/Comment                              |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit<br>of Biological Material          | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance             |
|   | 9. <input type="checkbox"/> Other _____  |

## **DETAILED ACTION**

### **EXAMINER'S AMENDMENT**

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee. Authorization for this examiner's amendment was given in a telephone interview with Jung-hua Kuo (Reg. No. 41,918) on 11/18/2005.

The application has been amended as follows:

#### **AMEND CLAIM 1 AS FOLLOWS:**

1. (Currently Amended) An electronics component mounting system, comprising:
  - a first mounting structure including:
    - two generally parallel and opposing spacers configured to space a first electronics component to be mounted to the first mounting structure from an electronics base, the first mounting structure being configured to be attached to the electronics base;
    - a plurality of supports extending from the spacers, the supports being configured to engage holes defined on a surface of the first electronics component; [[and]]
    - a base portion extending between the two generally parallel and opposing spacers, the spacers and the supports being generally orthogonal to the base portion; and

a space formed by the spacers, the base portion, and the electronics component when mounted to the first mounting structure to facilitate cooling of the electronics component when mounted to the first mounting structure.

**AMEND CLAIM 13 AS FOLLOWS:**

13. (Currently Amended) An electronics component mounting apparatus, comprising:
- component mounting means for securing a first electronics component to an electronics base, including:
- means for spacing the first electronics component from the electronics base, the spacing means being configured to form a space between the first electronics component to be mounted on the component mounting means [[from]] and the electronics base to facilitate cooling of the first electronics component when mounted to the component mounting means, the component mounting means being configured to be attached to the electronics base;
- means for engaging a plurality of holes defined on a surface of the first electronics component to be mounted on the mounting ~~mechanism~~ means, said means for engaging extends from said means for spacing; and
- means for securing the component mounting means to the first electronics component.

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**AMEND CLAIM 17 AS FOLLOWS:**

17. (Currently Amended) A method, comprising the steps of:
- positioning a first electronics component onto opposing spacers of a first mounting structure, the spacers being configured to form a space between the first electronics component to be mounted to the first mounting structure [[from]] and an electronics base to facilitate cooling of the first electronics component when mounted to the first mounting structure, the first mounting structure being configured to be attached to the electronics base;
- engaging a plurality of supports extending from the spacers with holes defined on a surface of the first electronics component such that the space is defined by the spacers and the first electronics component; and
- securing the first electronics component and the first mounting structure to each other.

***Allowable Subject Matter***

**Claims 1-22 are allowable.**

The following is an examiner's statement of reasons for allowance: In response to remarks and claim amendments made in Applicant's Amendment submitted 08/19/2005, as well as, an Interview on 08/18/2005, and, upon conclusion of a comprehensive search of the pertinent prior art, the Office indicates that the claims, as amended, are allowable.

**Regarding claim 1**, patentability exists, at least in part, with the claim recitation of a space formed by the spacers, the base portion, and the electronics component when

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mounted to the first mounting structure to facilitate cooling of the electronics component when mounted to the first mounting structure.

**Regarding claim 13**, patentability exists, at least in part, with the claim recitation of means for spacing the first electronics component from the electronics base, the spacing means being configured to form a space between the first electronics component to be mounted on the component mounting means and the electronics base to facilitate cooling of the first electronics component when mounted to the component mounting means, the component mounting means being configured to be attached to the electronics base.

**Regarding claim 17**, patentability exists, at least in part, with the claim recitation of engaging a plurality of supports extending from the spacers with holes defined on a surface of the first electronics component such that the space is defined by the spacers and the first electronics component; and securing the first electronics component and the first mounting structure to each other.

Sheldon et al US Patent 6599136, Lerude et al US Patent 4630175, Astier et al US Patent 5788211 and Cheng et al US Patent 6804123 are all cited as teaching elements of the claimed invention including two generally parallel and opposing spacer structures, a plurality of supports extending from the spacers and a base portion extending between and the spacers and supports being generally orthogonal to the base portion. The prior art, alone, or, in combination does not, however, teach or suggest all the elements of the claimed invention arranged in the manner as claimed by the Applicants.

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Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

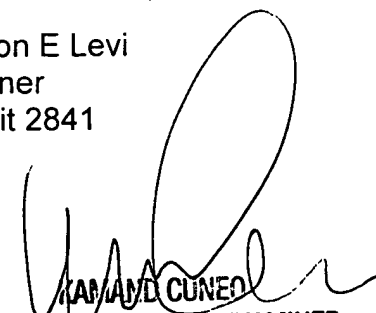
Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dameon E. Levi whose telephone number is (571) 272-2105. The examiner can normally be reached on Mon.-Fri. (9:00 - 5:00).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kamand Cuneo can be reached on (571) 272-1957. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

DEL

Dameon E Levi  
Examiner  
Art Unit 2841



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SUPERVISORY PATENT EXAMINER  
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